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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

*None* *rd*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-028077 02/04/2000

*Yes* *rd*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 03/08/2001

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 4
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## TITLE

Delay adjustment circuit and a clock generating circuit using the same

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